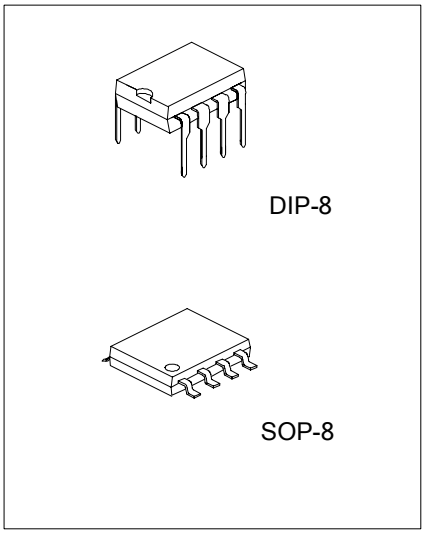
DUAL DIFFERENTIAL COMPARATOR

DESCRIPTION

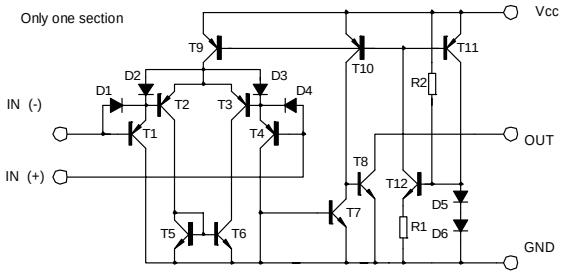
The UTC393 consists of two independent voltage comparators designed specifically to operate from a single power supply over a wide voltage range.

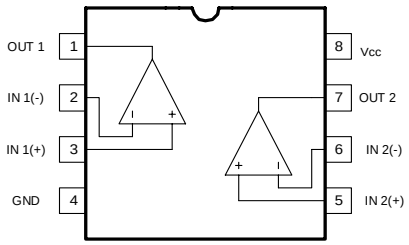


FEATURES

*Single or dual supply operation
*Wide operating supply range
(Vcc=2V~36V or ±1 to ±18V)
*Input common-mode voltage includes ground
*Low supply current drain: Icc=0.8mA(Typical)
*Low input bias current Ibias=25nA(Typical)
*Output compatiable with TTL,DTL,and CMOS logic system

BLOCK DIAGRAM





ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

Characteristic	Symbol	Value	Unit
Supply Voltage	Vcc	±18 OR 36	V
Differential input voltage	Vi(diff)	36	V
Input Voltage	VI	-0.3~36V	V
Power Dissipation	Pd	570	mW
Operating Temperature	Topr	0 to +70	°C
Storage Temperature	Tstg	-65 to 150	°C

ELECTRICAL CHARACTERISTICS

(Vcc=5.0V, Ta=25°C,All voltage referenced to GND unless otherwise specified)

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Input offset voltage	V_{IO}	V_{CM}=0 to Vcc-1.5 Vo(p)=1.4V,Rs=0		±1.0	±5.0	mV
Input offset current	I_{IO}			±5	±50	nA
Input Bias current	Ib			65	250	nA
Input Common-mode voltage range	$V_{I(R)}$		0		Vcc-1.5	V
Supply Current	Icc	R_L=∞		0.6	1.0	mA
		R_L=∞,Vcc=30V		0.8	2.5	mA
Large signal Voltage Gain	G_V	Vcc=15V,R_L>15kΩ	50	200		V/mV
Large signal response time	tres	Vi=TTL logic wing Vref=1.4V,V_{RL}=5V,R_L=5.1kΩ		350		ns
Response time	tres	V_{RL}=5V,R_L=5.1kΩ		1400		ns
Output sink current	Isink	$V_{i(-)}$>1V,$V_{i(+)}$=0V,Vo(p)<1.5V	6	18		mA
Output saturation voltage	Vsat	$V_{i(-)}$>1V,$V_{i(+)}$=0V,Isink=4mA	160	400		mV
output leakage current	$I_{leakage}$	$V_{I(+)}$=1V,$V_{I(-)}$=0,Vo(p)=5V	0.10			nA

TYPICAL CHARACTERISTICS PERFORMANCE

Fig.1 supply current

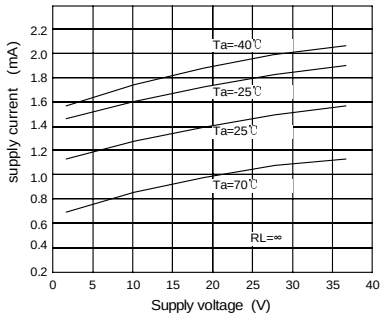


Fig.2 Input current

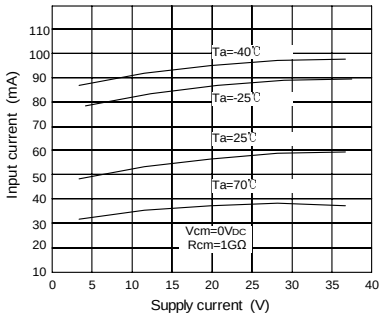


Fig.3 Output saturation voltage

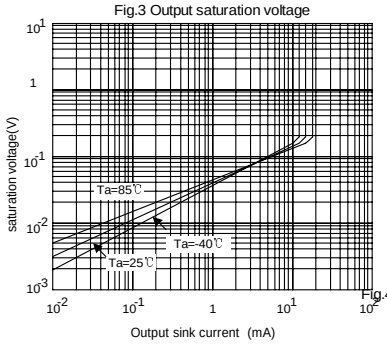


Fig.4 Reponse time for various input overdrive negative transition

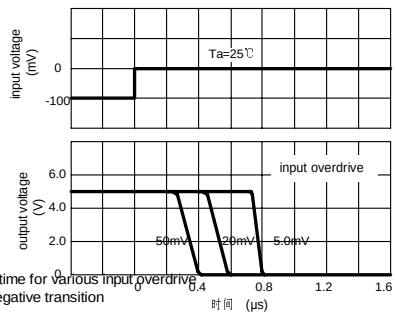


Fig.4 Reponse time for various input overdrive negative transition

Fig.4 Reponse time for various input overdrive positive transition

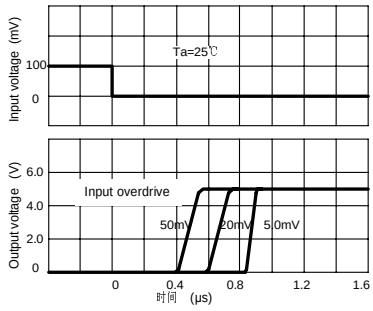


Fig.7

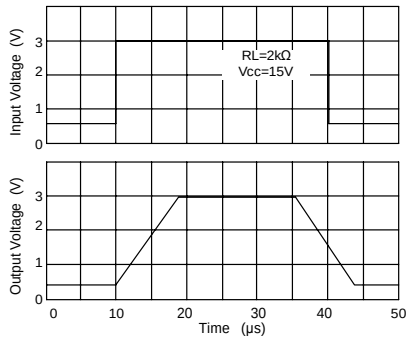


Fig.8 voltage Follower pulse response (small signal)

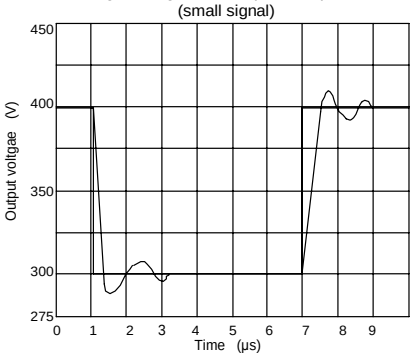


Fig.9 Large signal Frequency Response

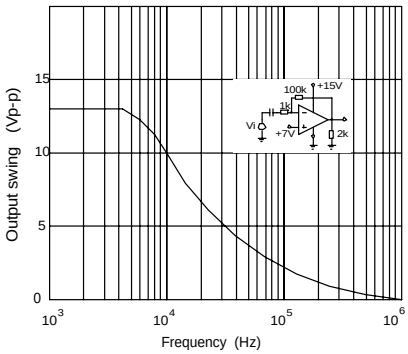


Fig.10 Output Characteristics current sourcing

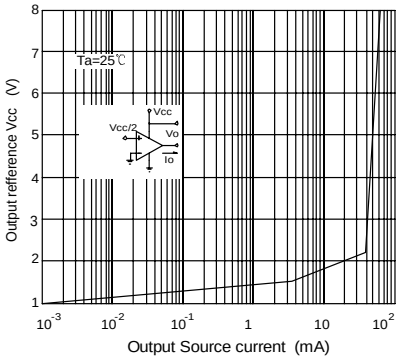


Fig.11 Output Characteristics Current sinking

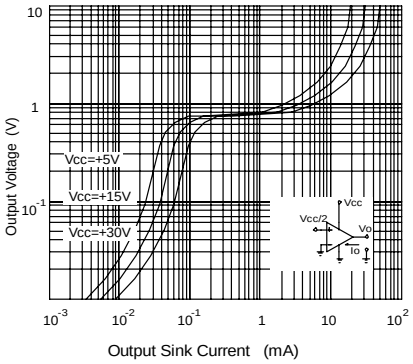
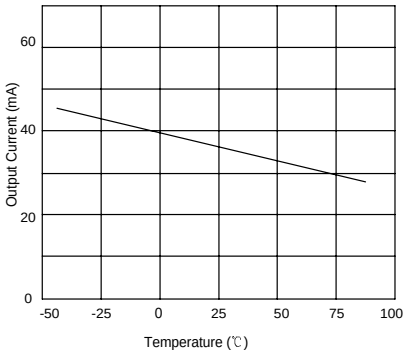
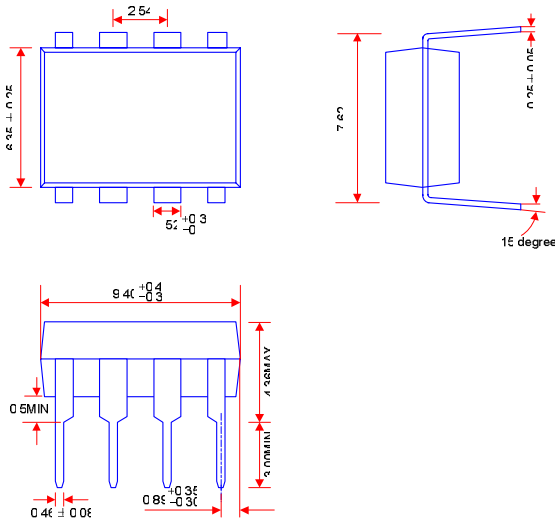


Fig.12 Current Limiting

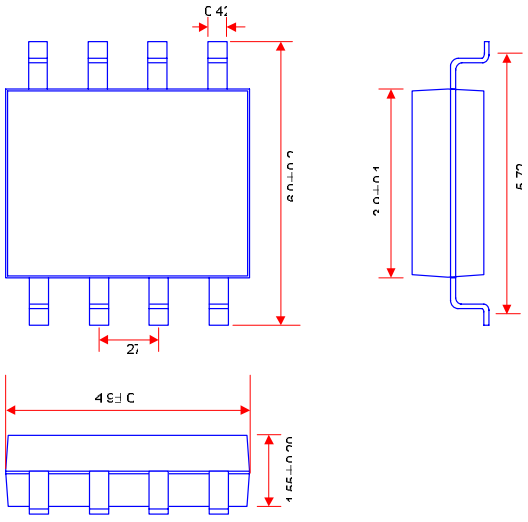


PACKAGE OUTLINE

DIP-8-300-2.54 Unit: mm



SOP-8-225-1.27 UNIT: mm



Attach

Revision History

Data	REV	Description	Page
	1.0	Original	
2005.05.19	1.1	Revise the title	1
2005.10.12	1.2	Add “DIP-8,SOP-8” package outline	5